



Title: VOLTAGE TOLERANT CIRCUIT FOR PROTECTING AN INPUT BUFFER
Inventor: Reed et al.
App. No: 10/630,949 Docket No: ARTCP042

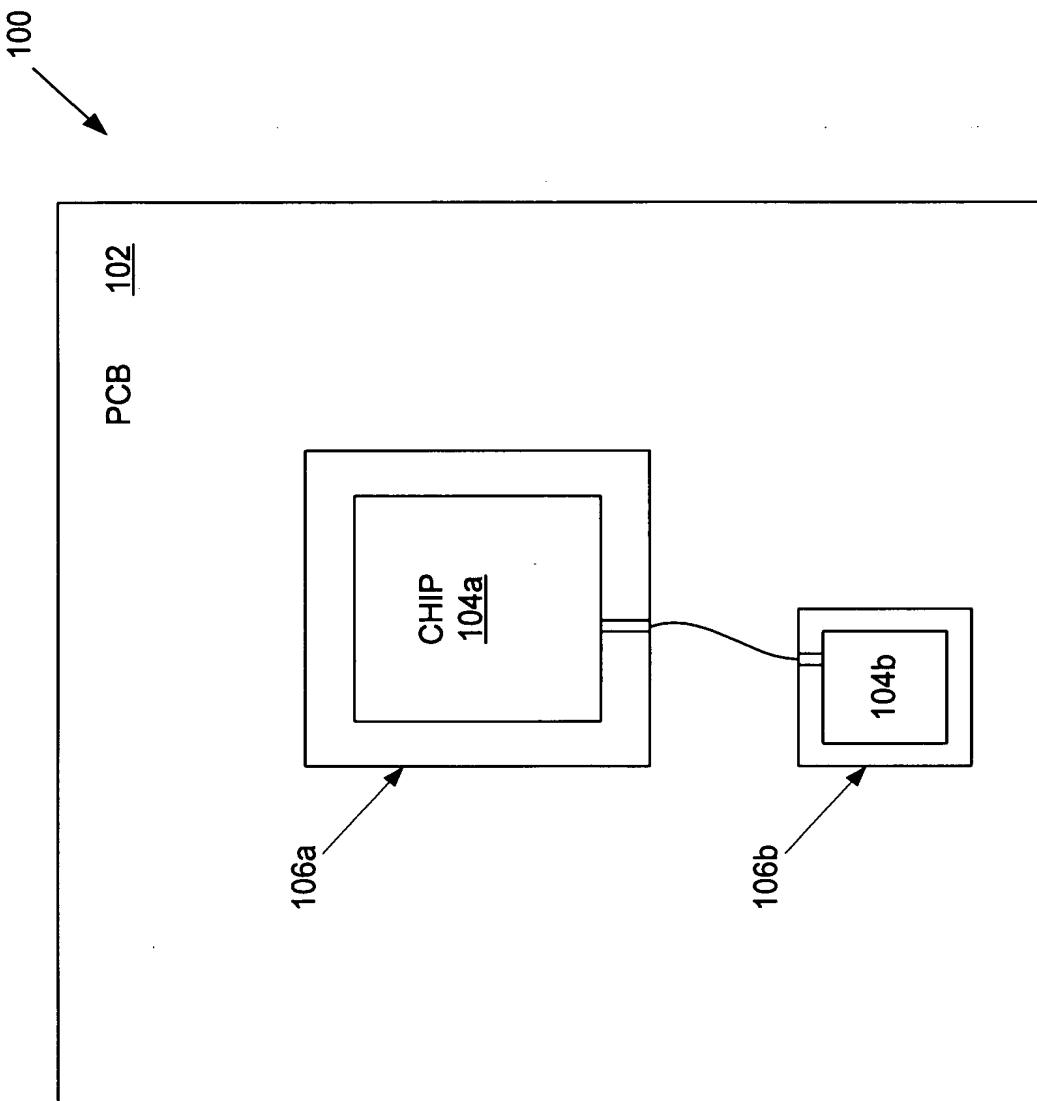


FIG. 1
(Prior Art)

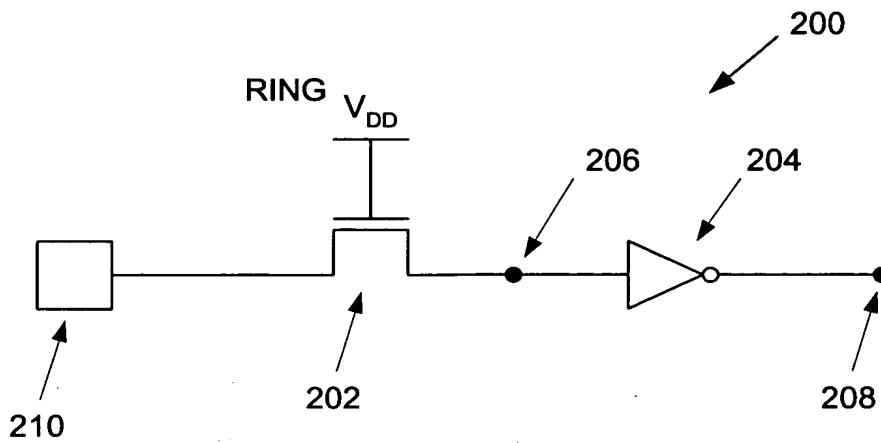


FIG. 2A
(Prior Art)

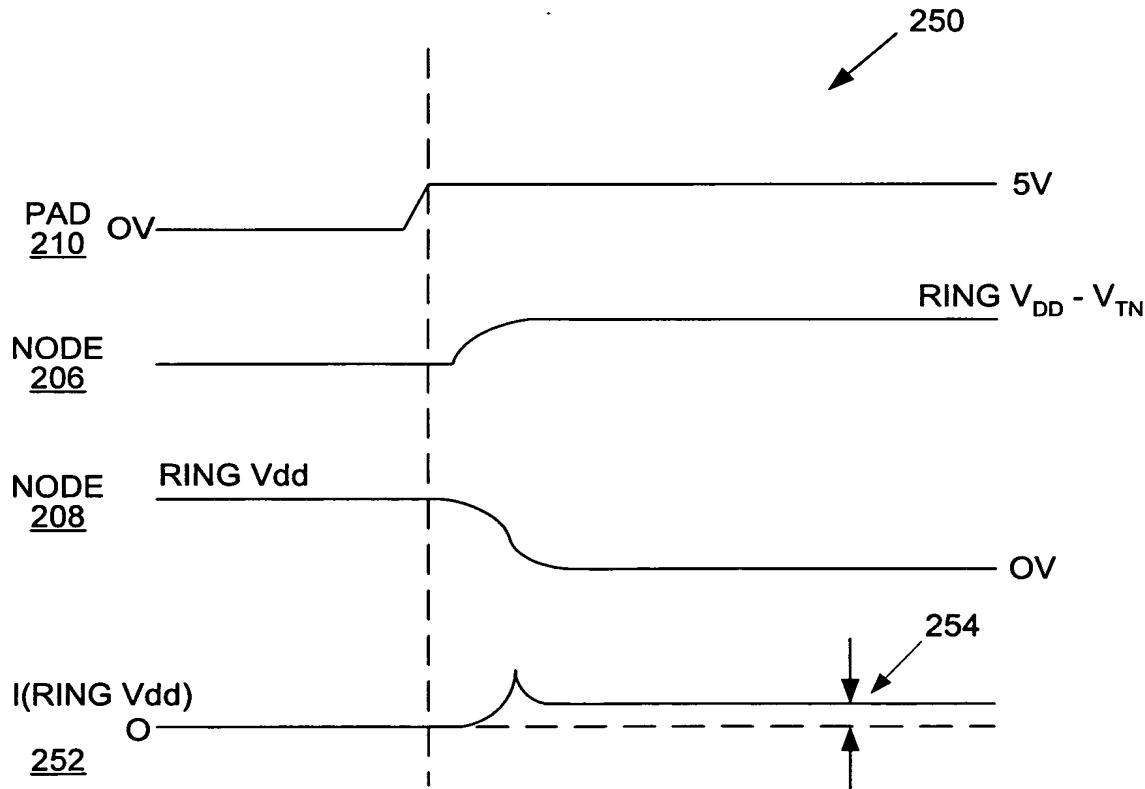


FIG. 2B
(Prior Art)

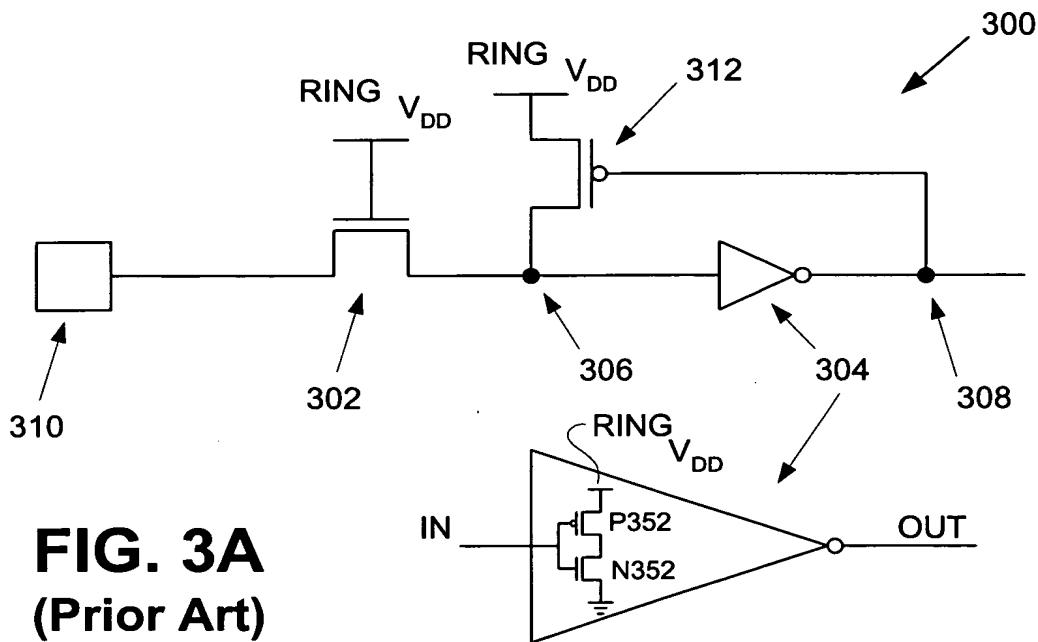


FIG. 3A
(Prior Art)

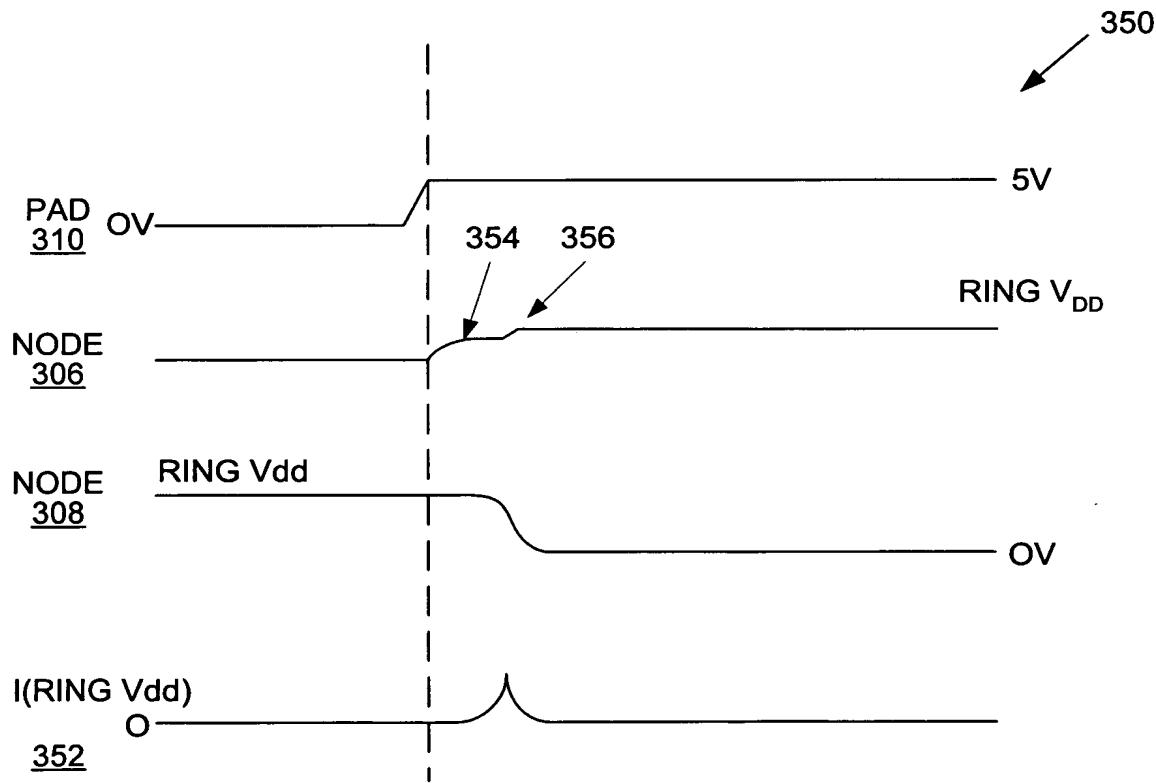


FIG. 3B
(Prior Art)

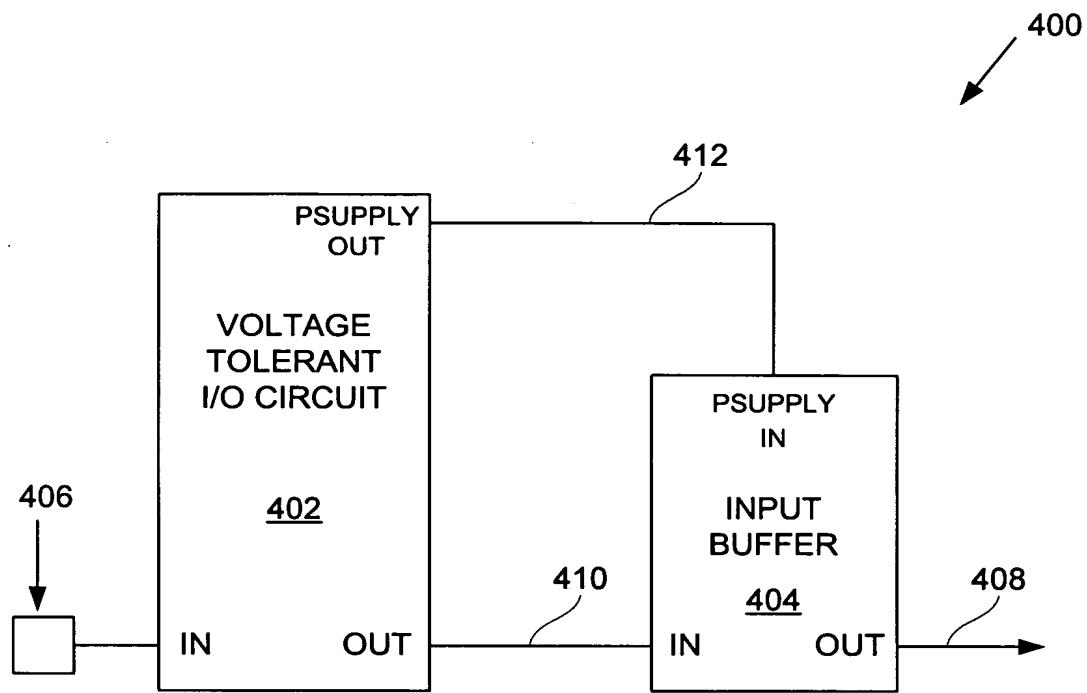


FIG. 4

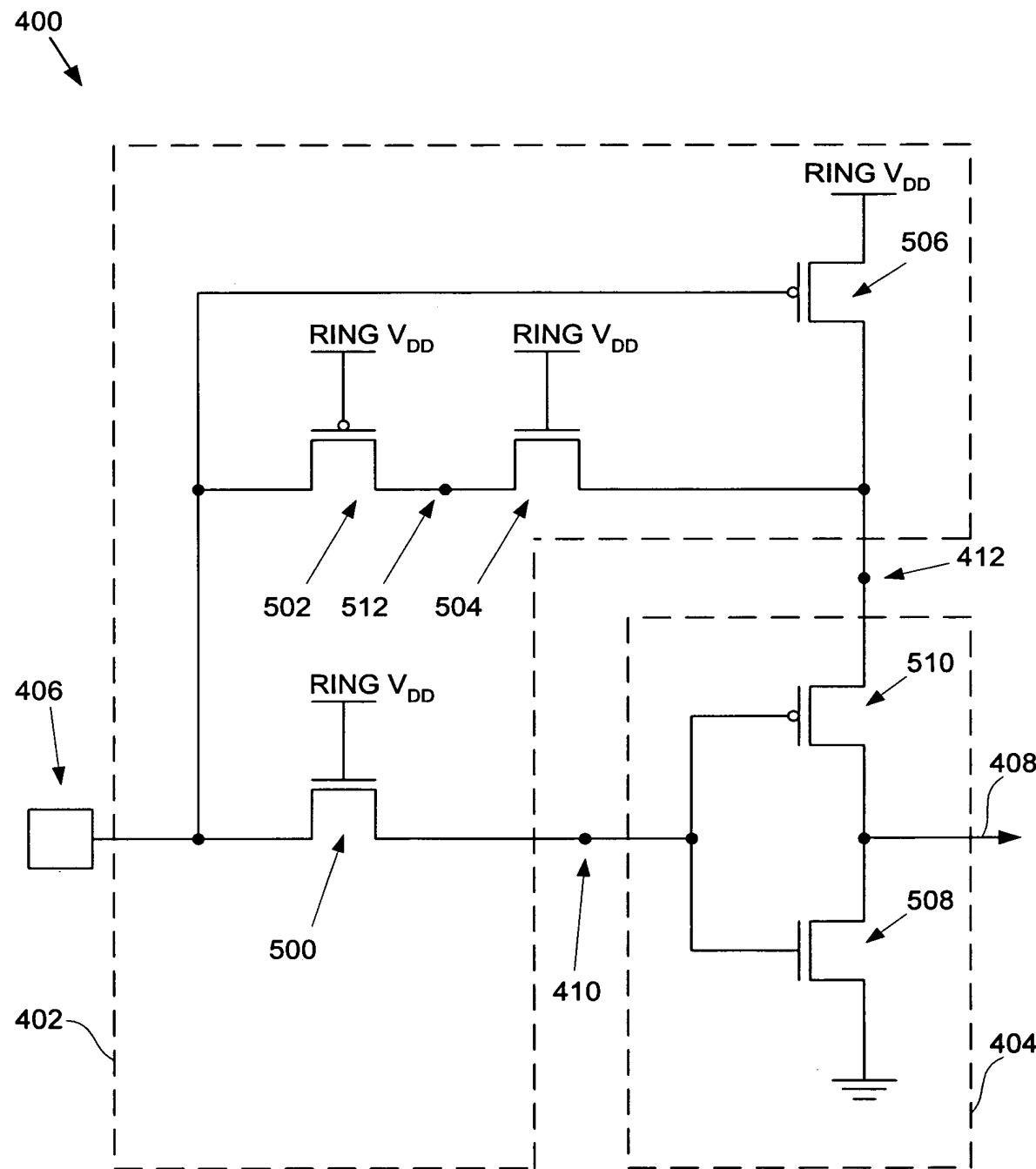


FIG. 5

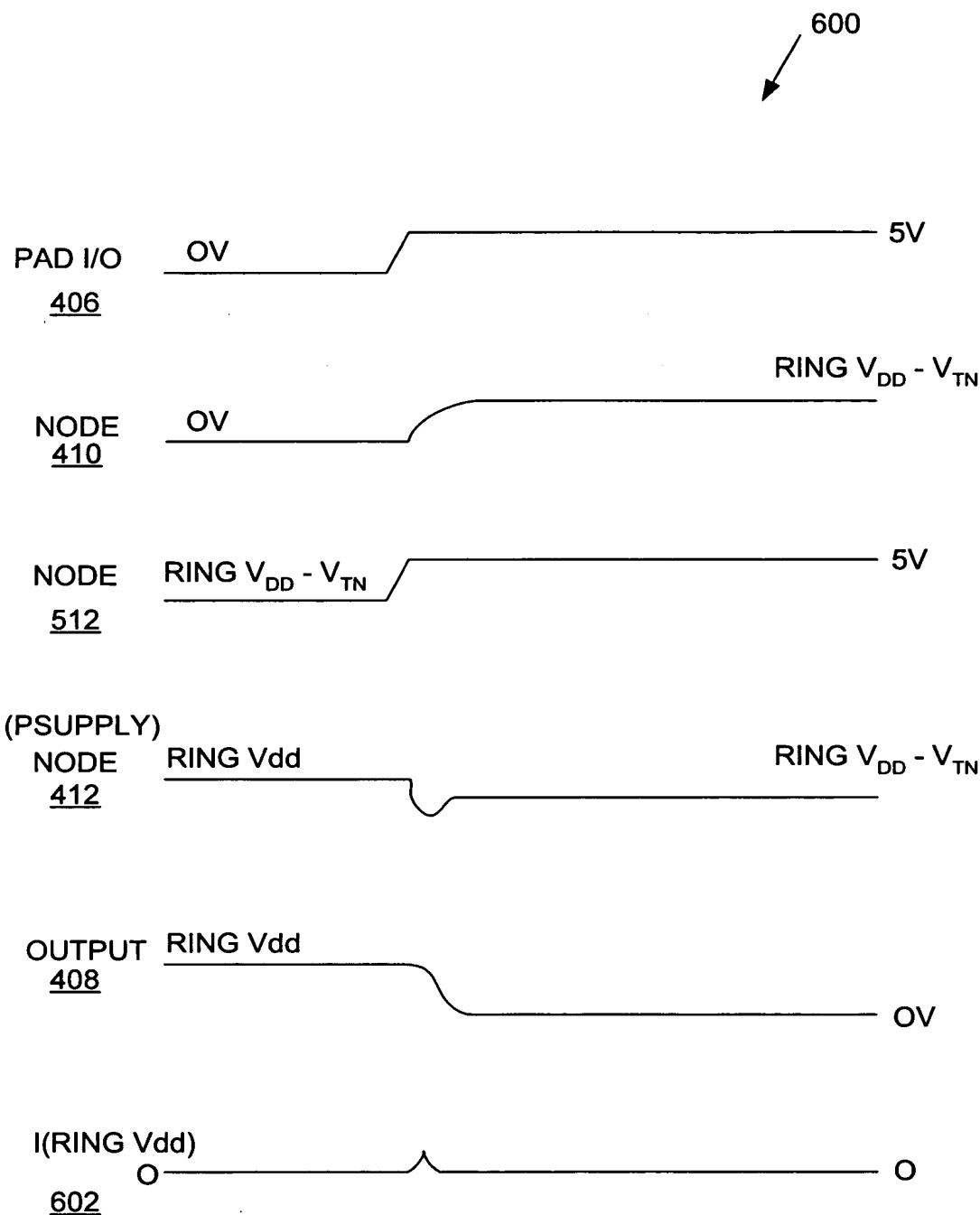


FIG. 6

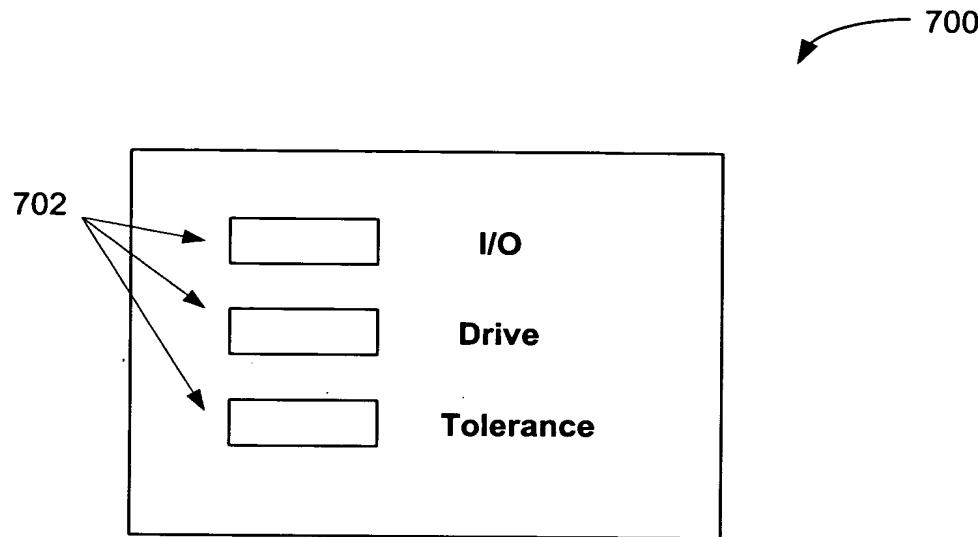


FIG. 7A

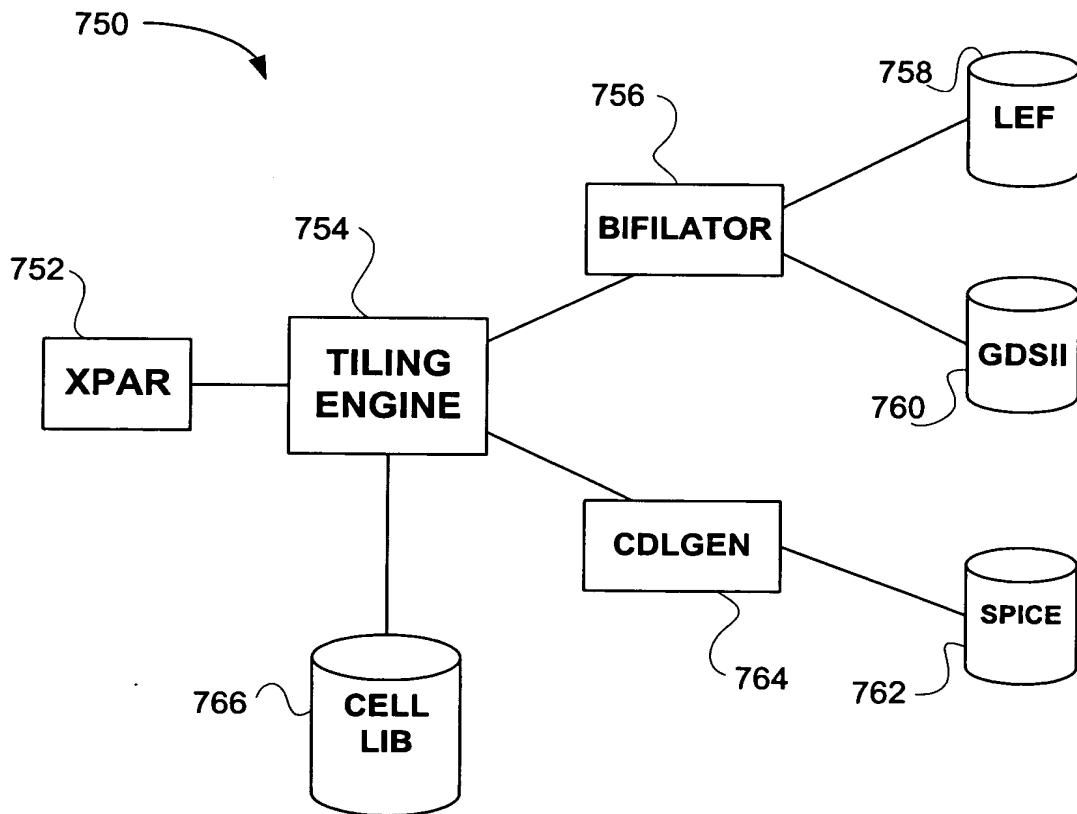


FIG. 7B